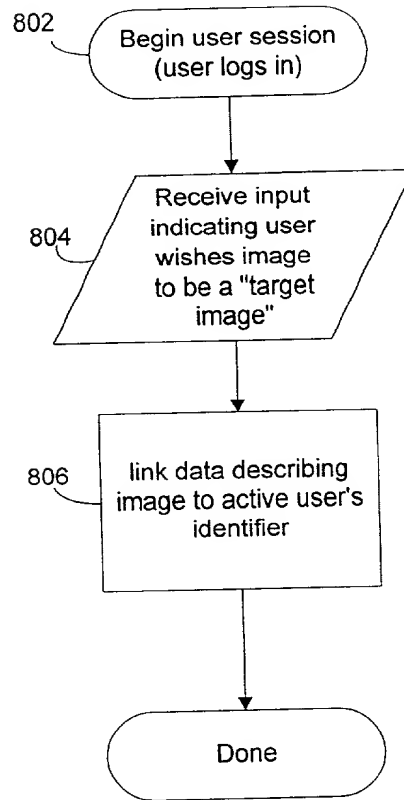


FIG. 1



**FIG. 2**

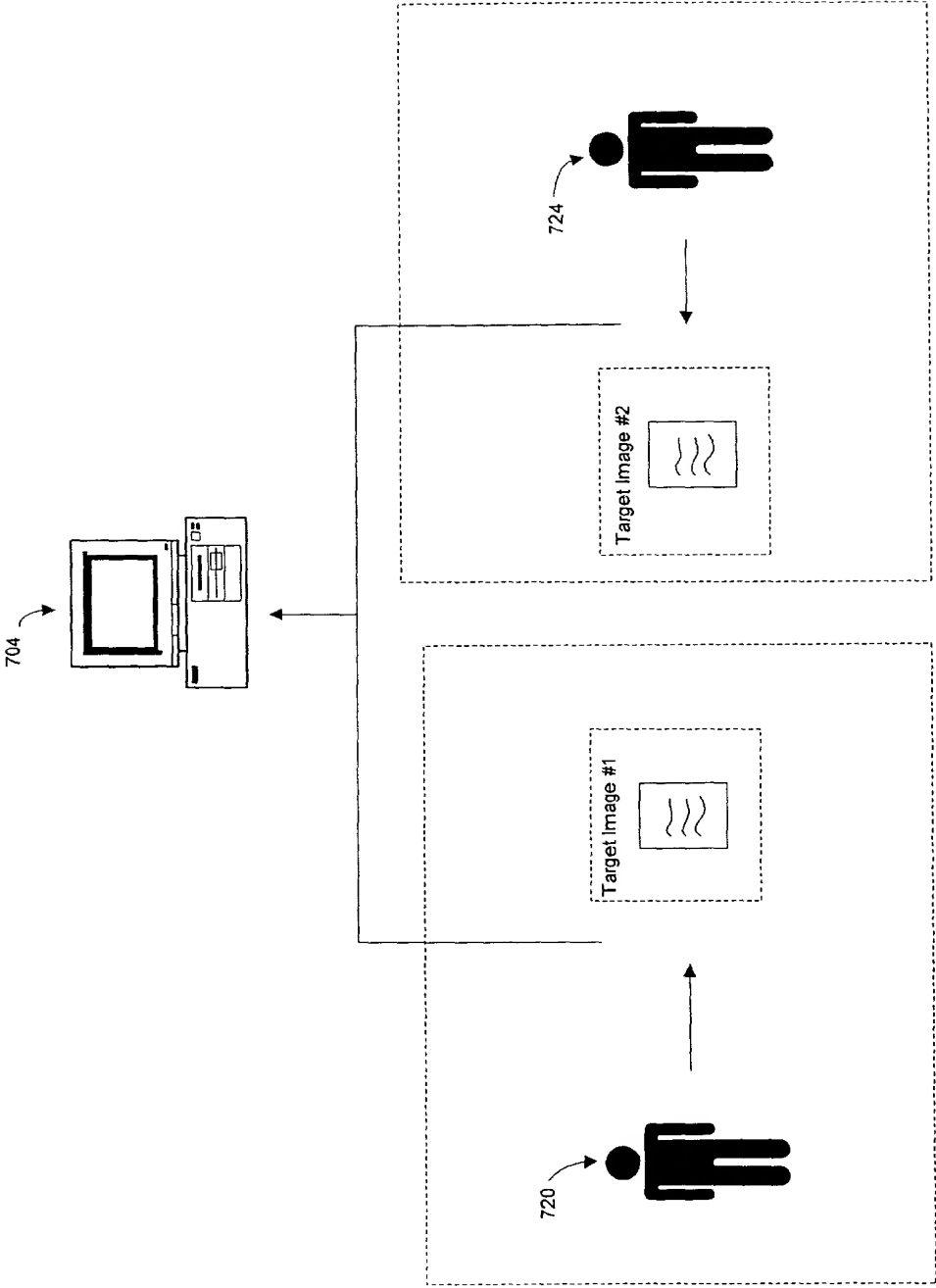


FIG. 3

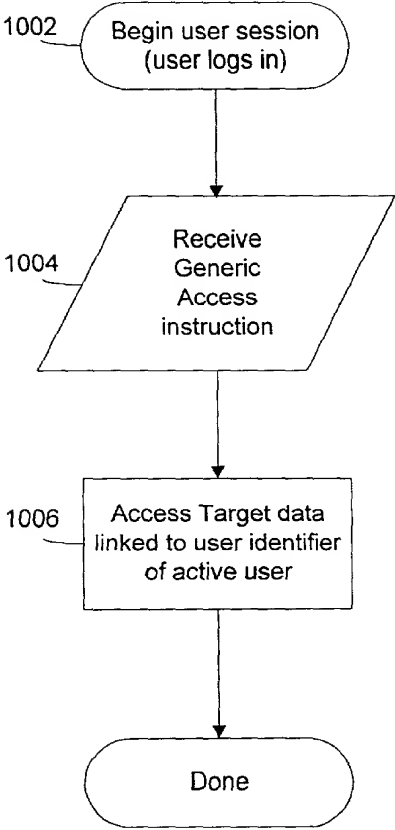


FIG. 4

FIG. 5 is a block diagram of a first web site 708. The first web site 708 includes a microprocessor(s) 1102, memory 1104, a web server 1110, a first web page 1112, and document creation content 1114. The first web site 708 is connected to an I/O port 1106, which is connected to a communication link 712.

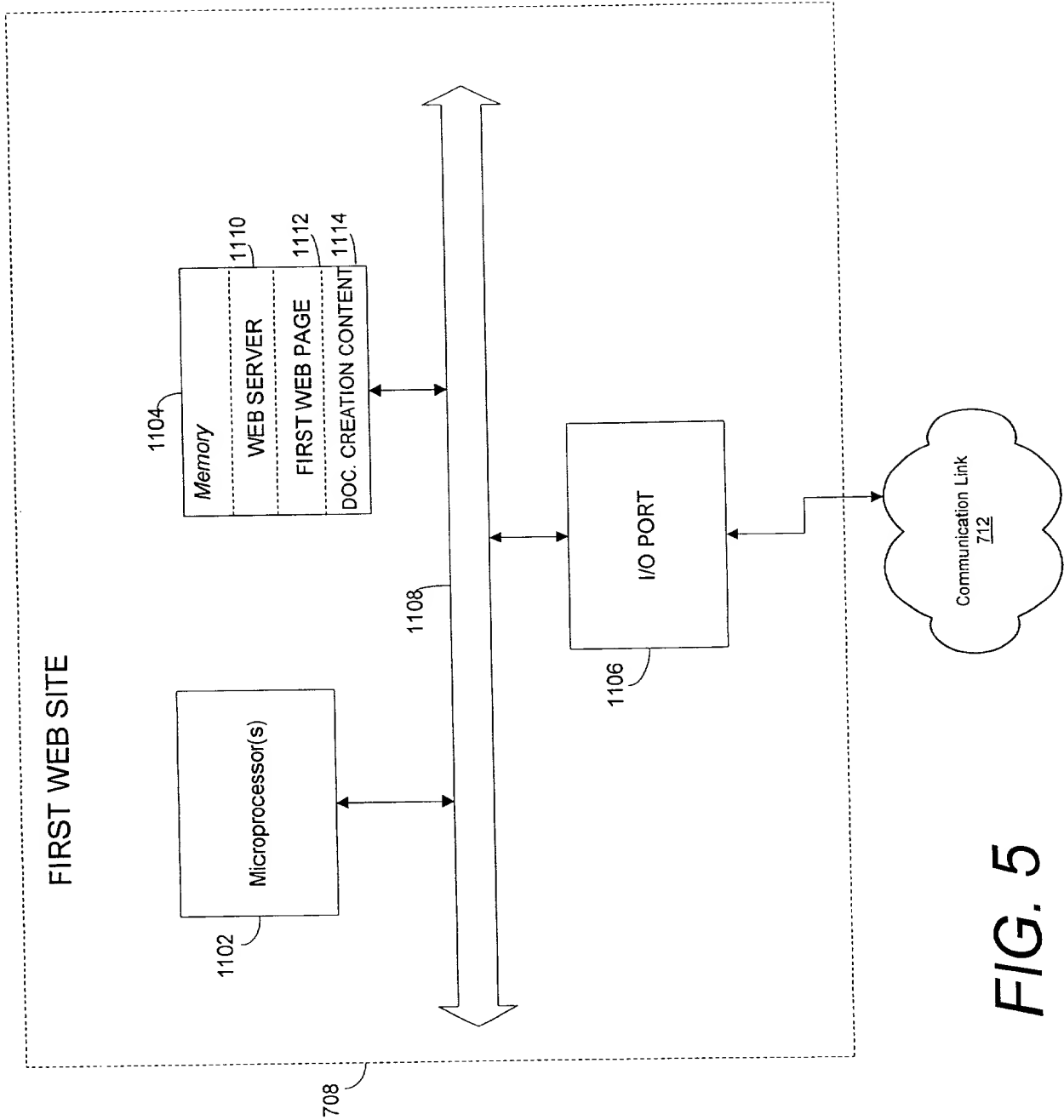


FIG. 5

FIG. 6

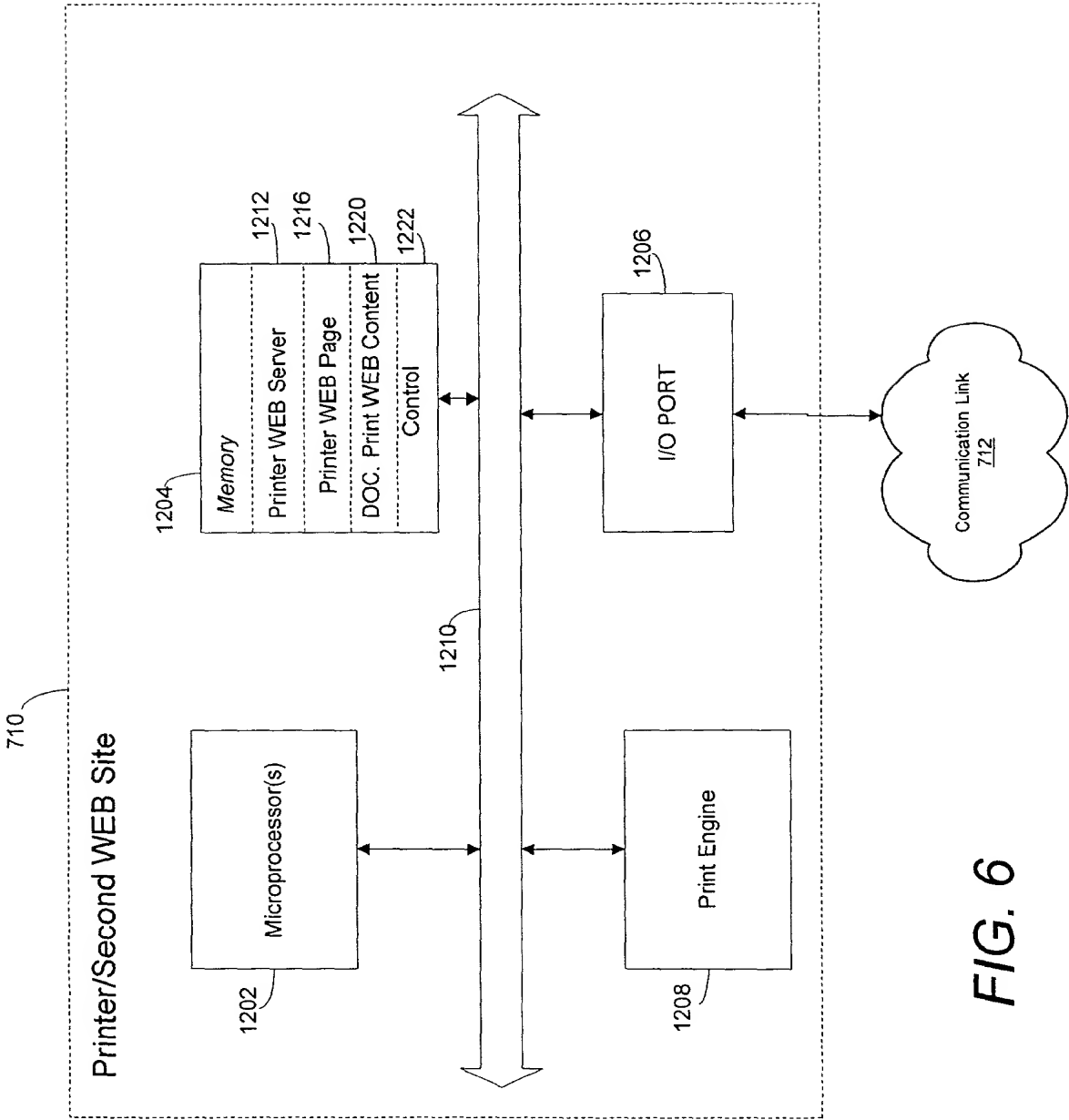


FIG. 6

FIG. 7

706

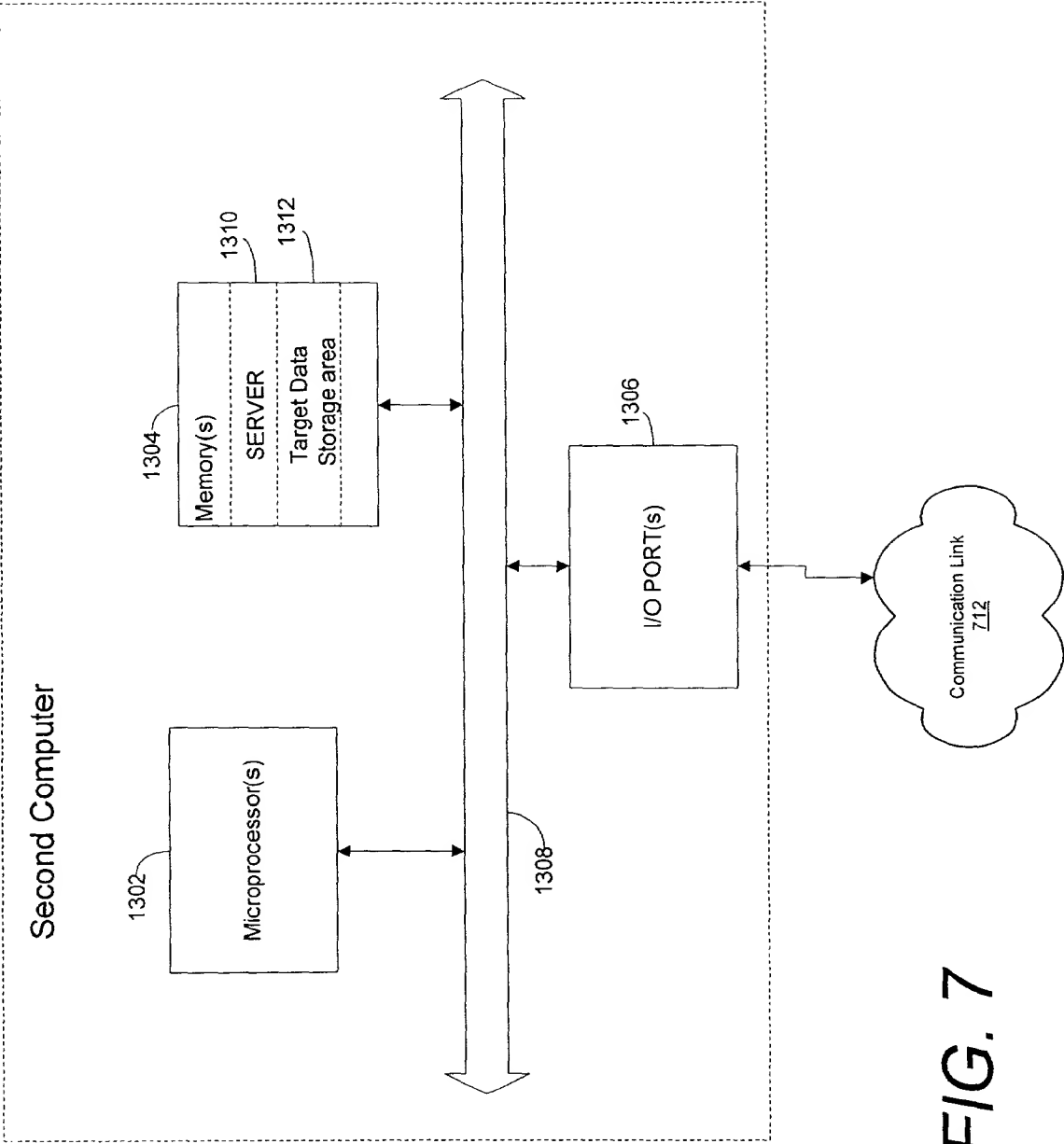


FIG. 7

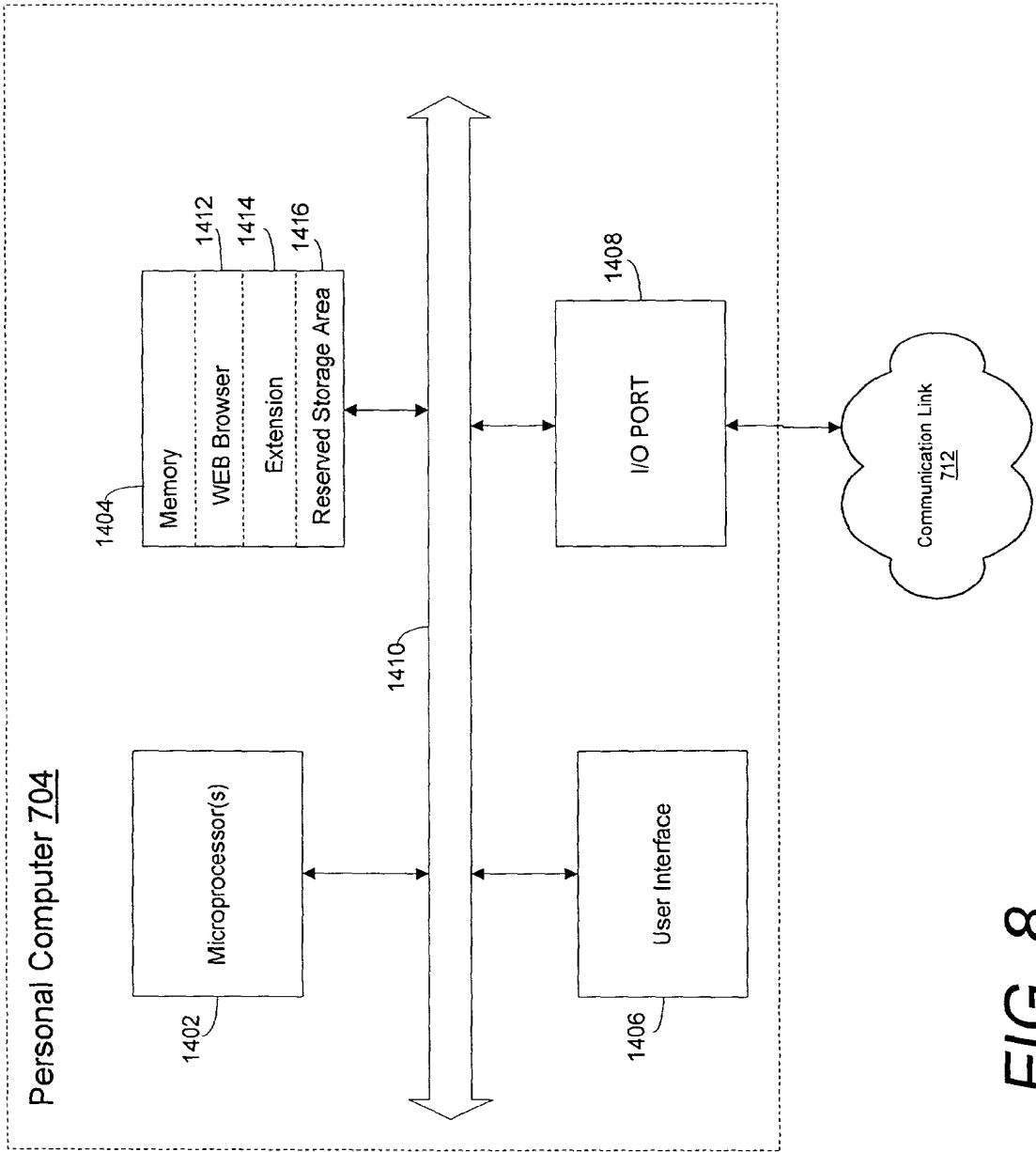


FIG. 8



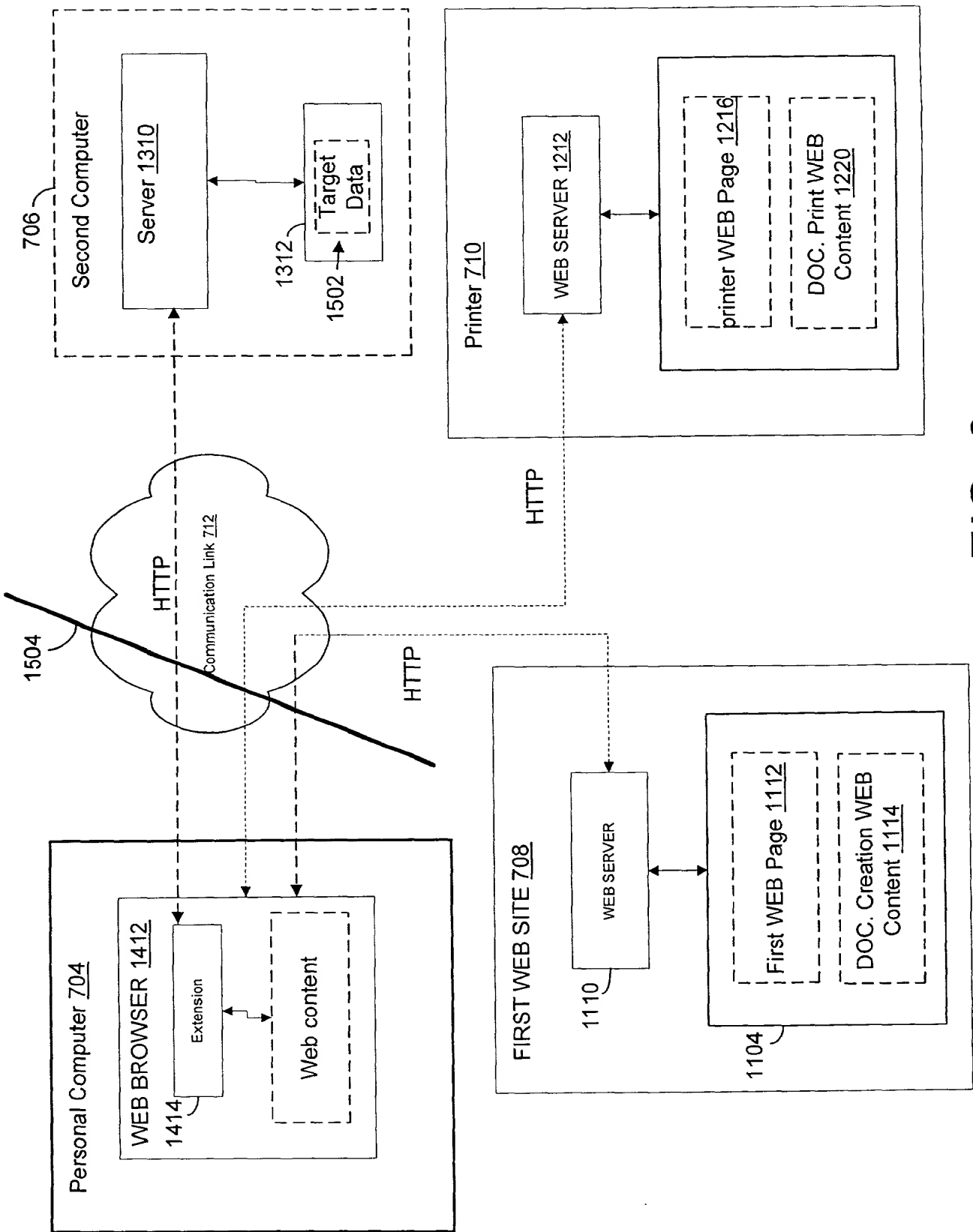


FIG. 9

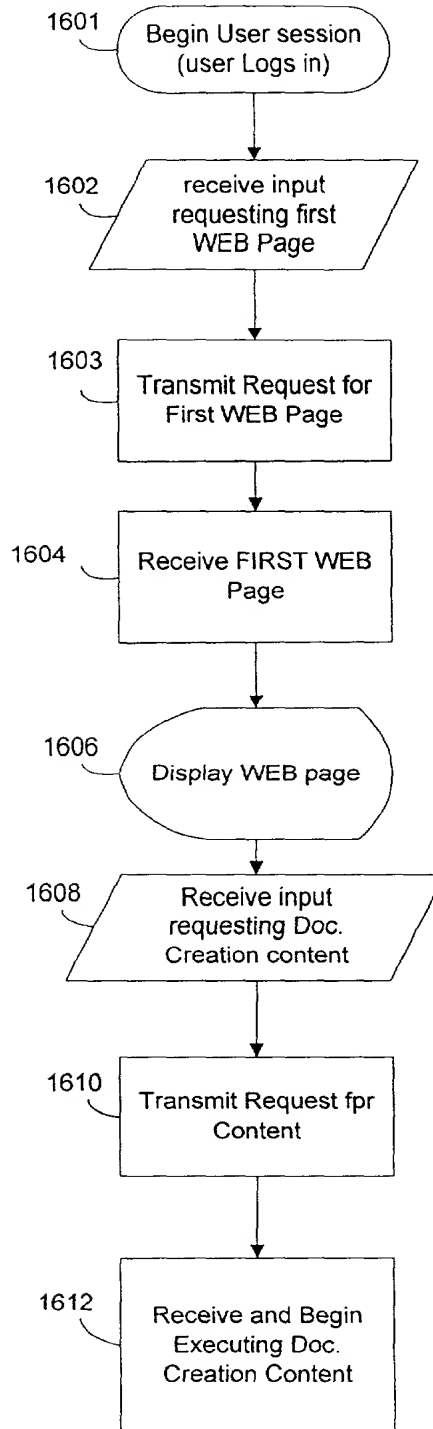
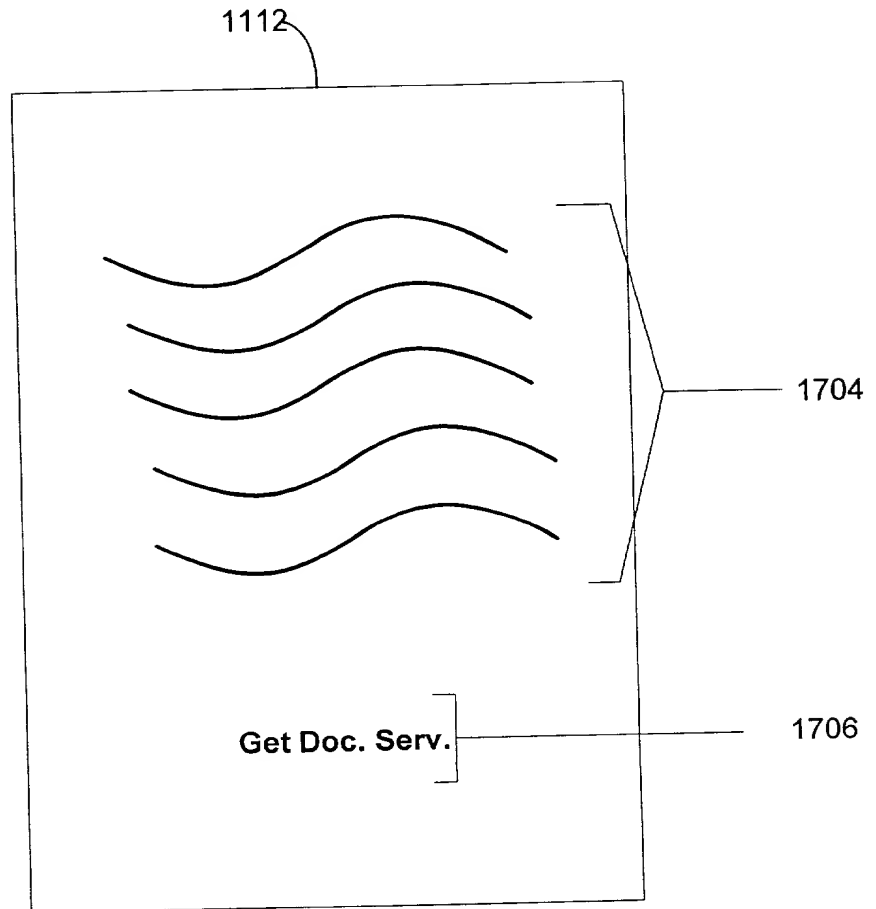
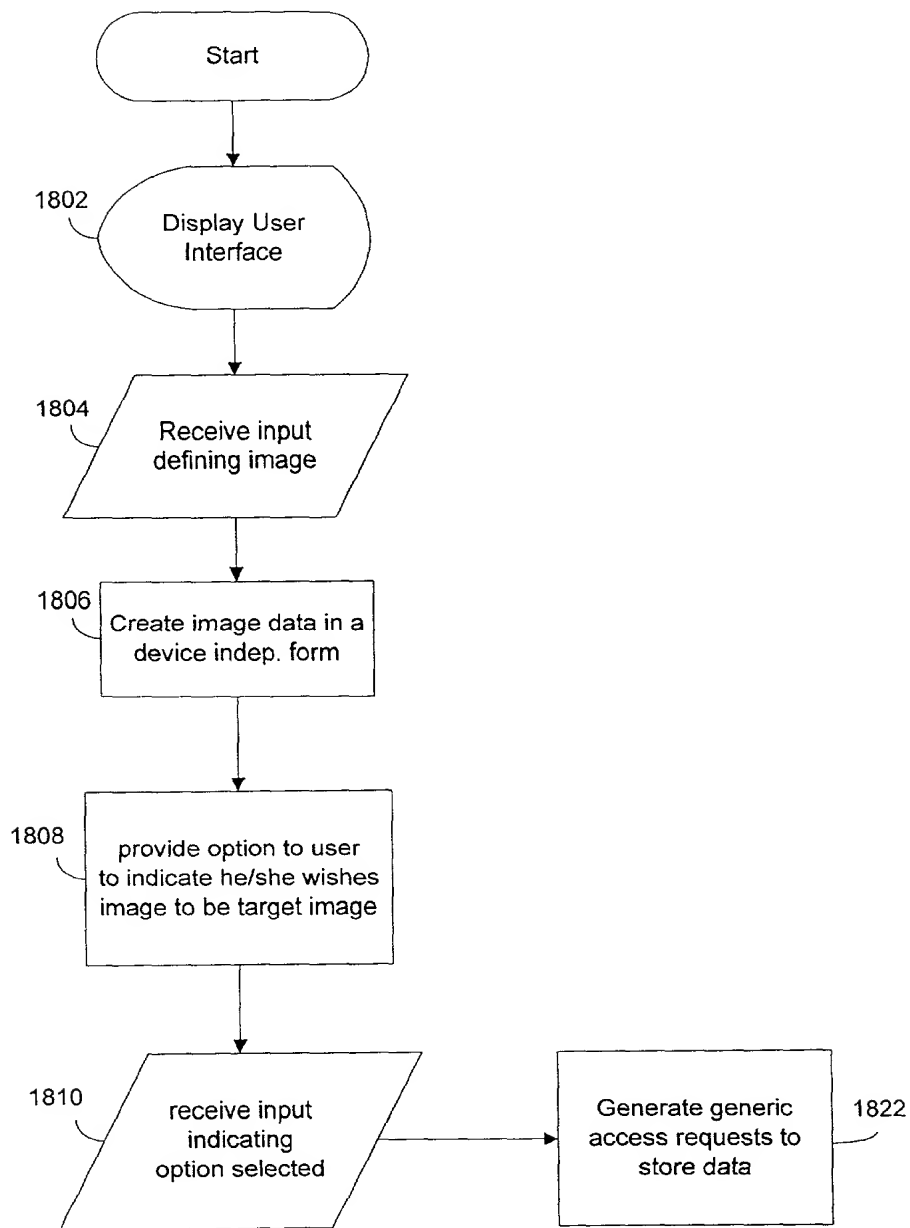


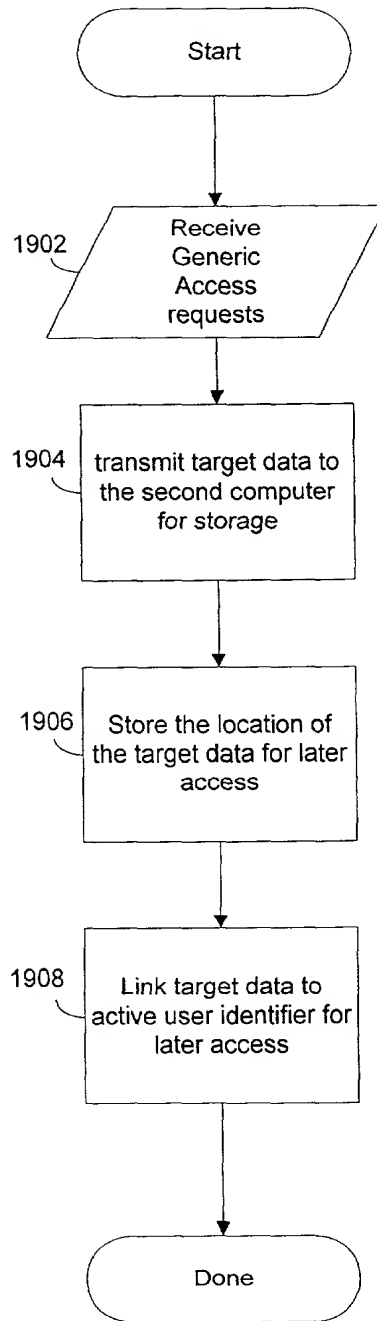
FIG. 10



**FIG. 11**



**FIG. 12**



**FIG. 13**

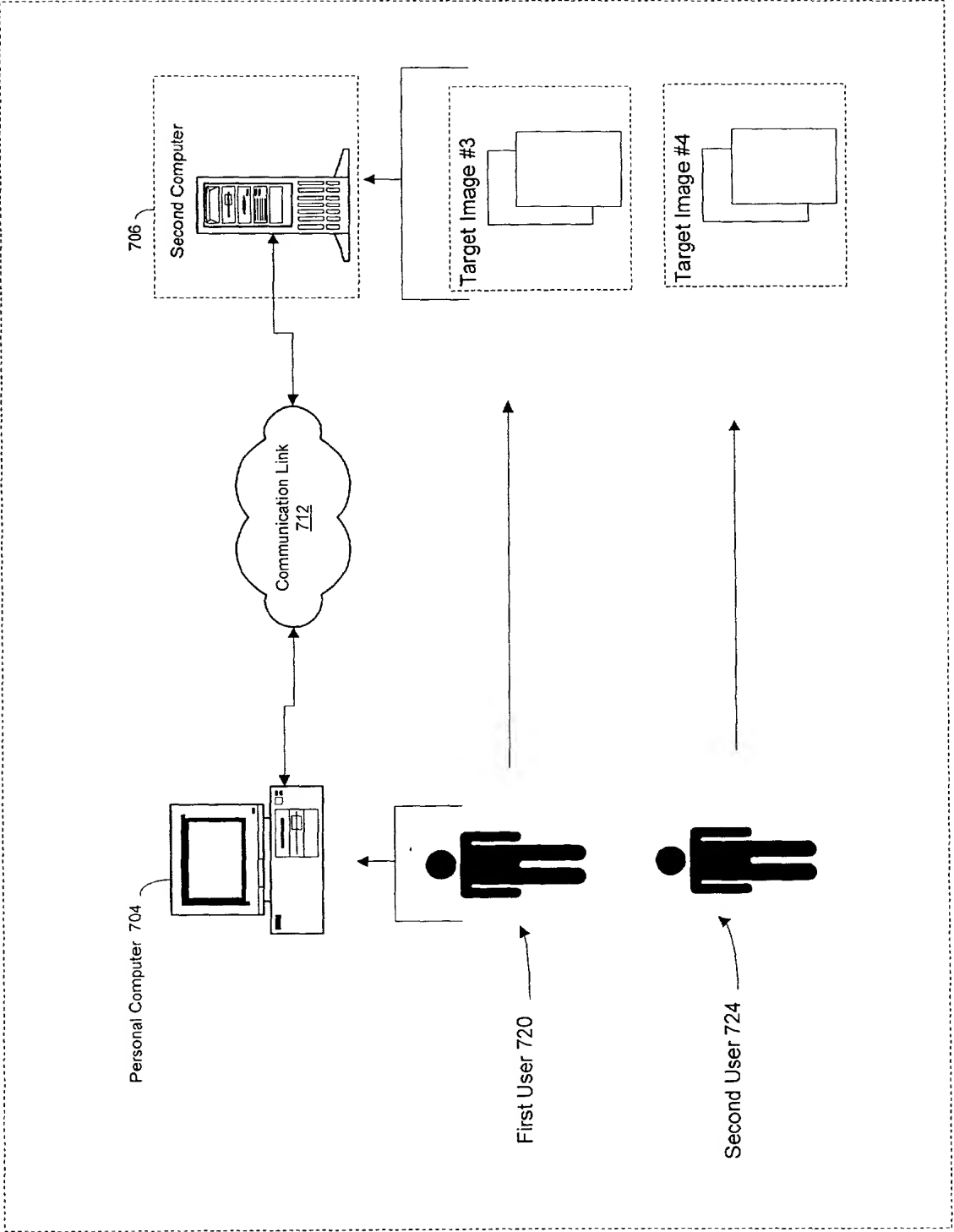
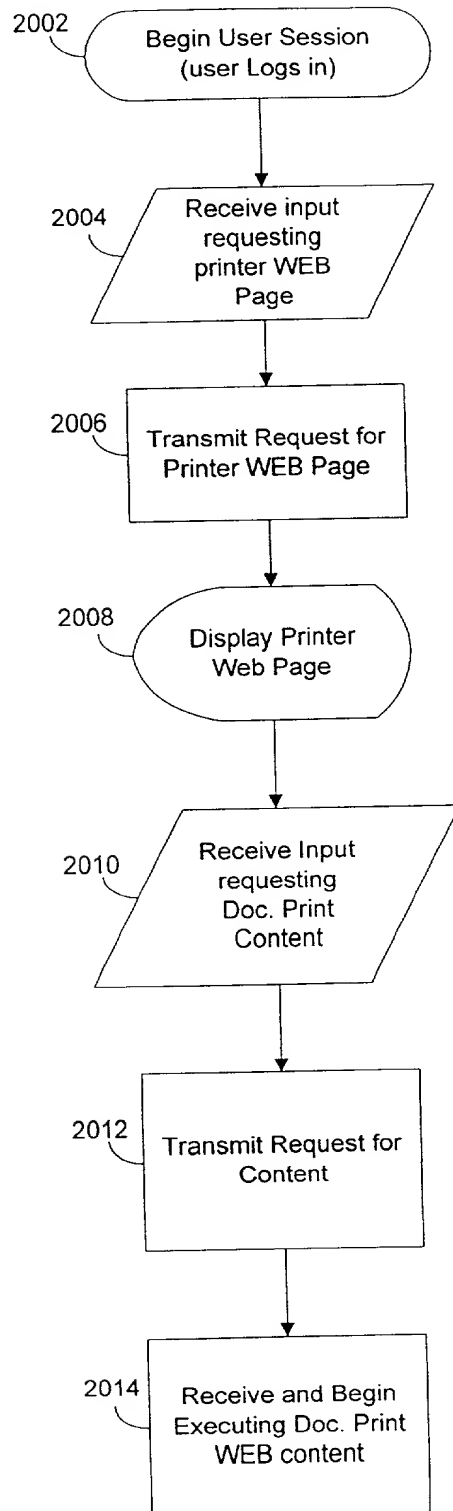
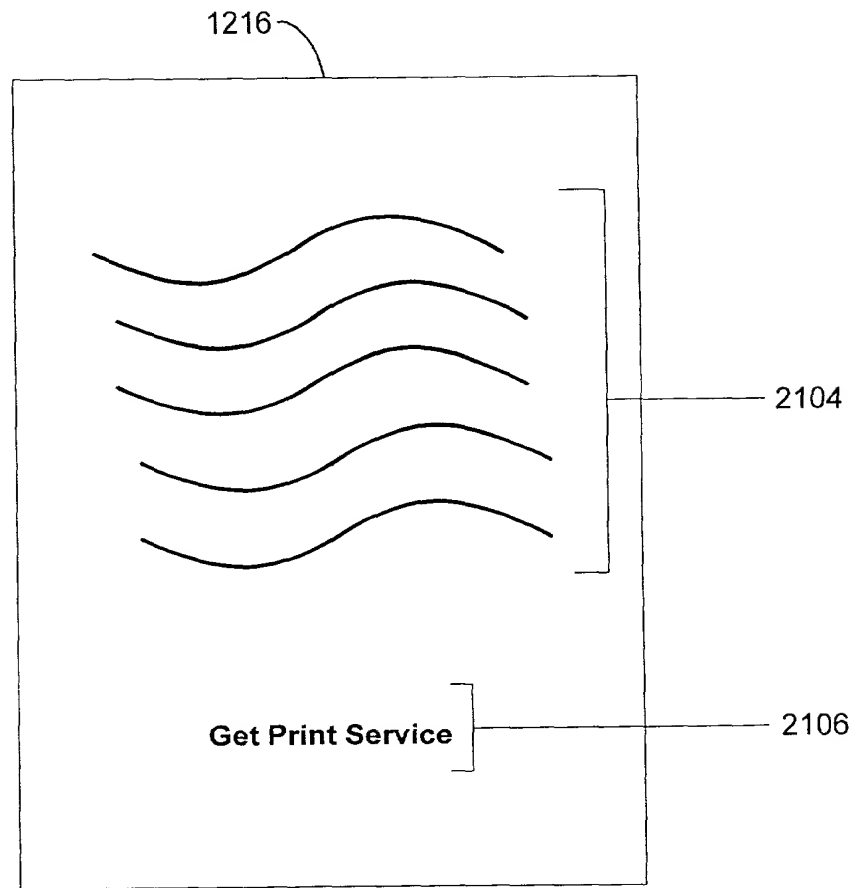


FIG. 14



**FIG. 15**



**FIG. 16**



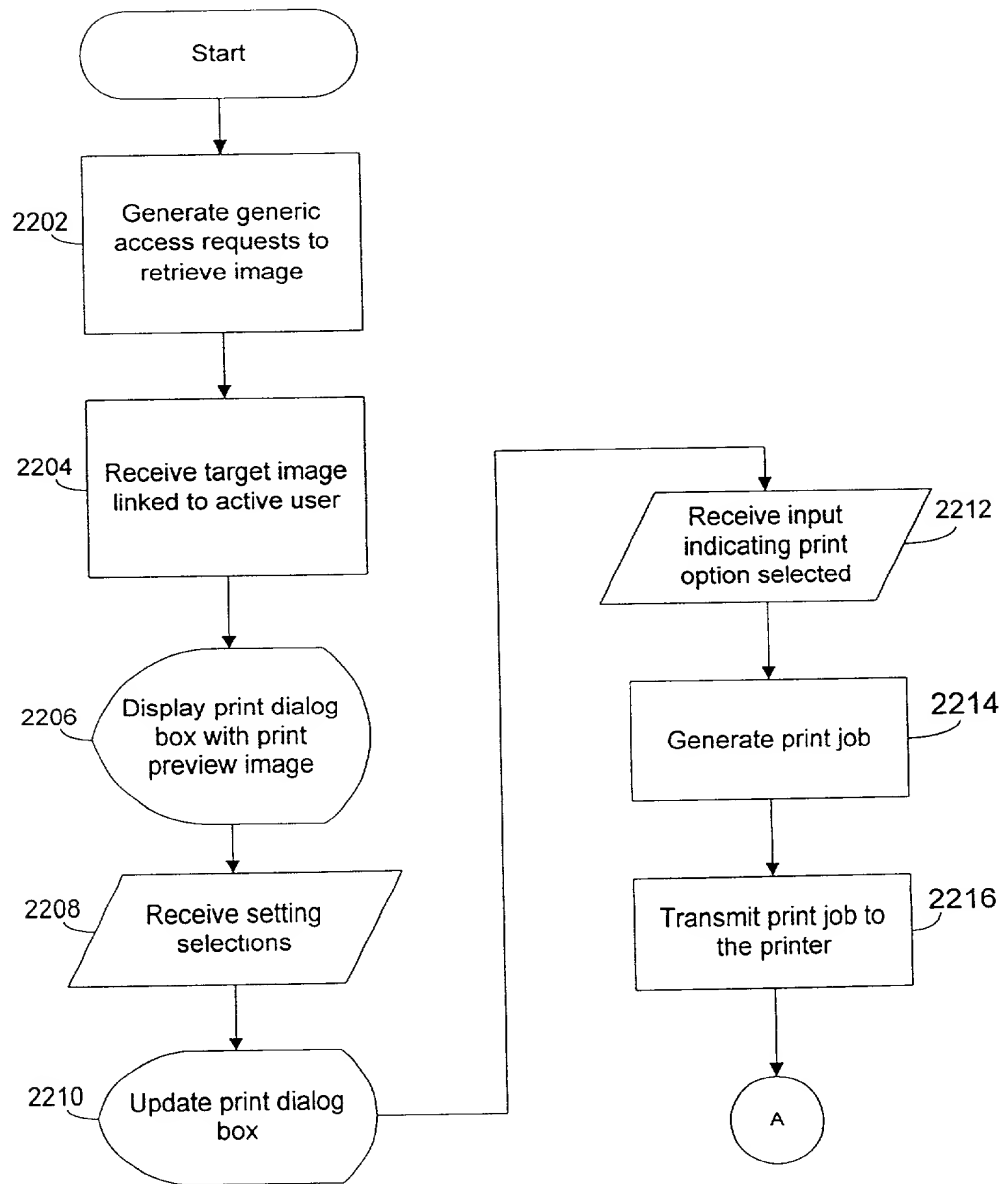


FIG. 17

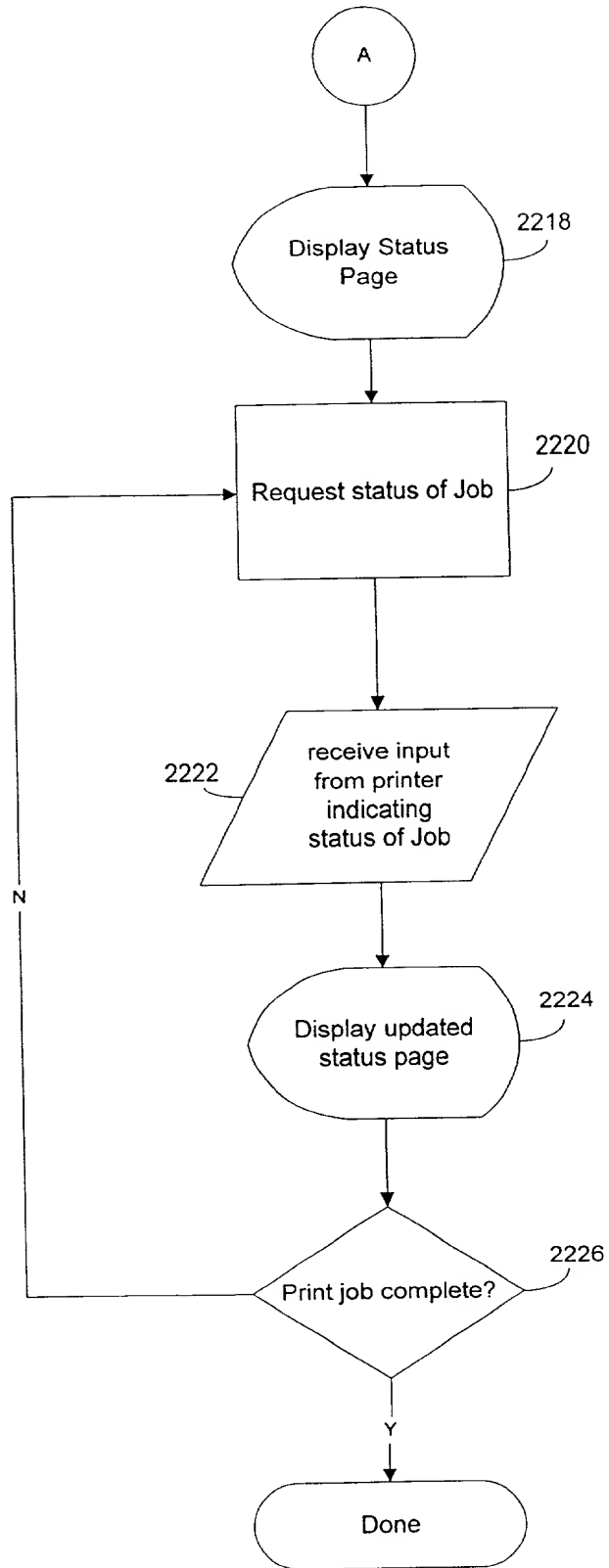
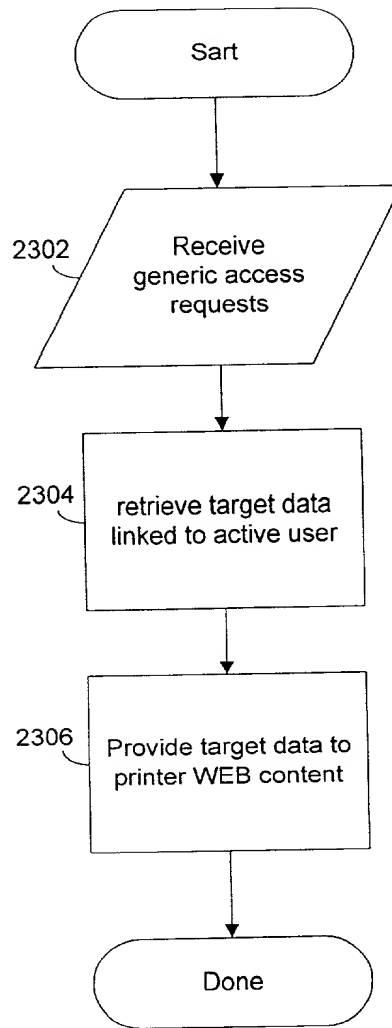


FIG. 18



**FIG. 19**

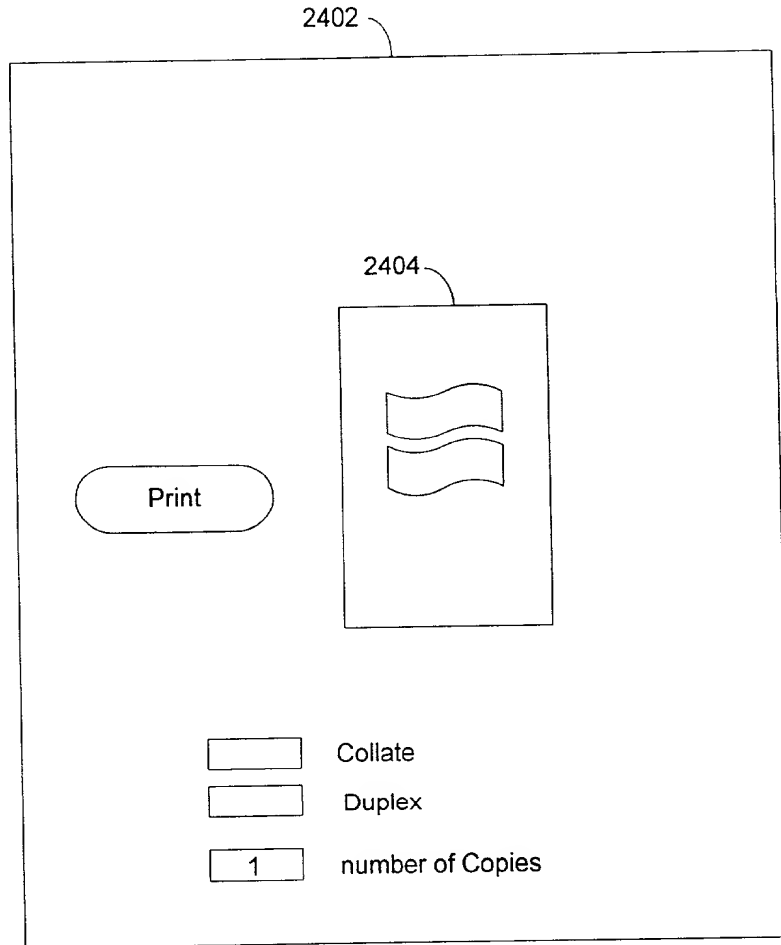


FIG. 20

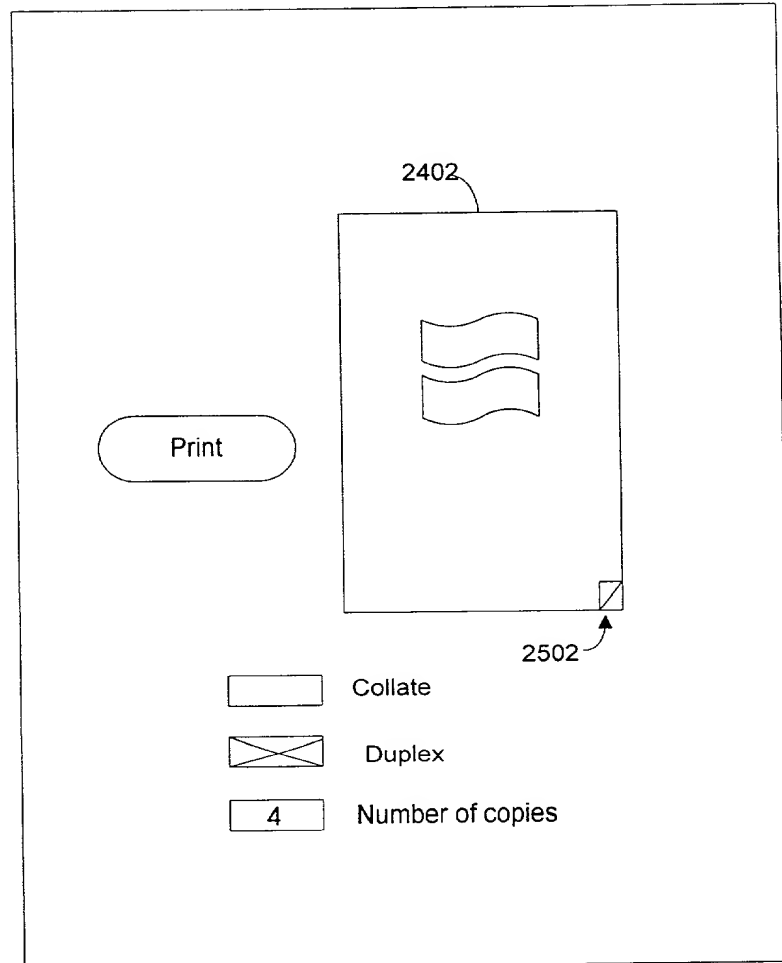
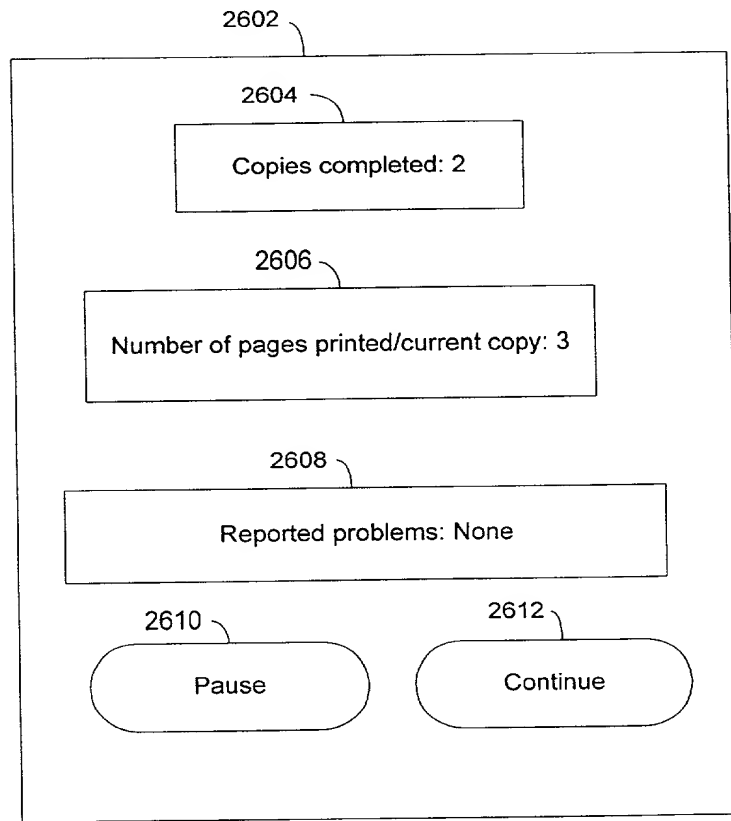


FIG. 21



**FIG. 22**

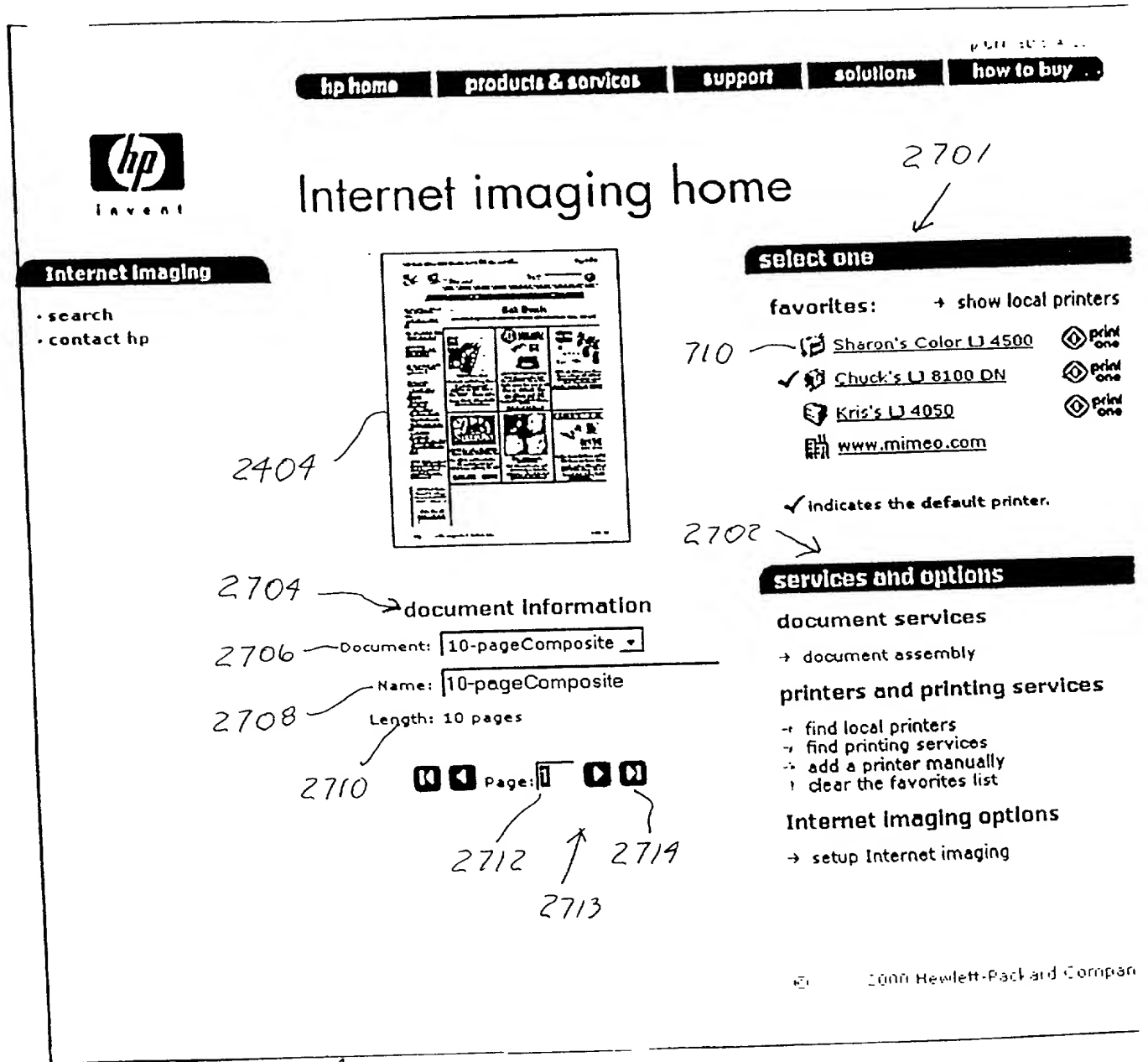


FIG. 23

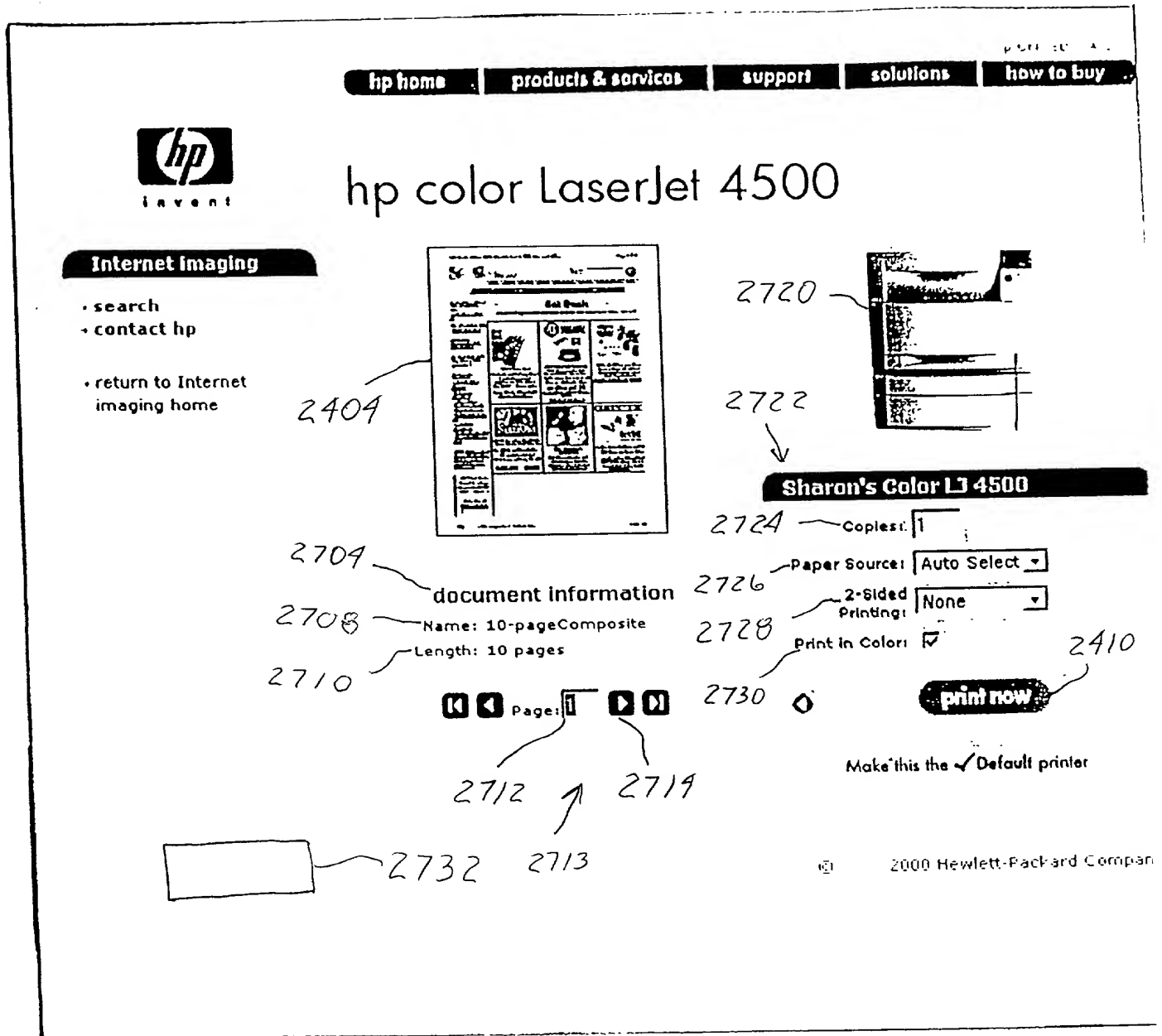


FIG. 24



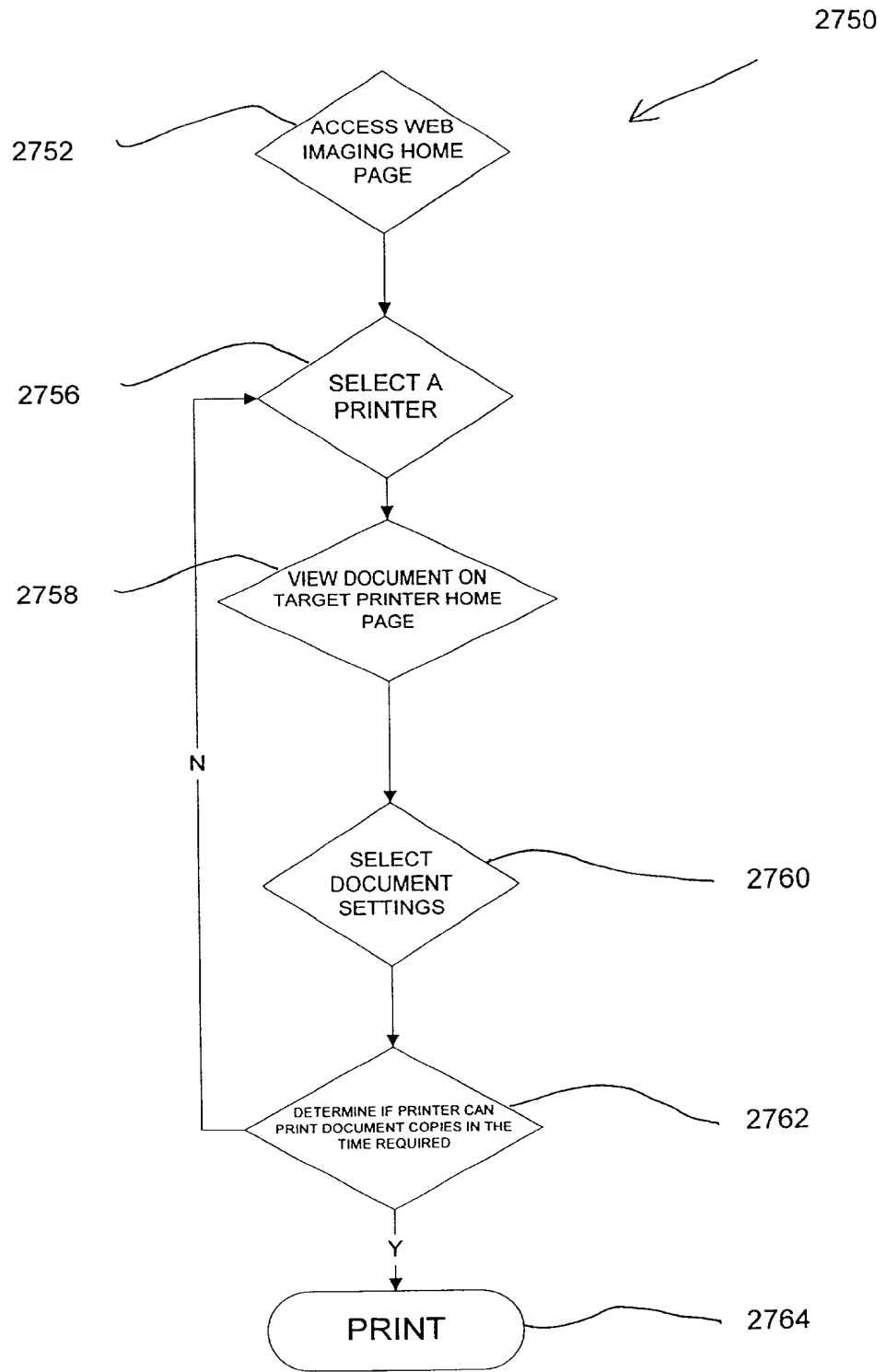


FIG. 25